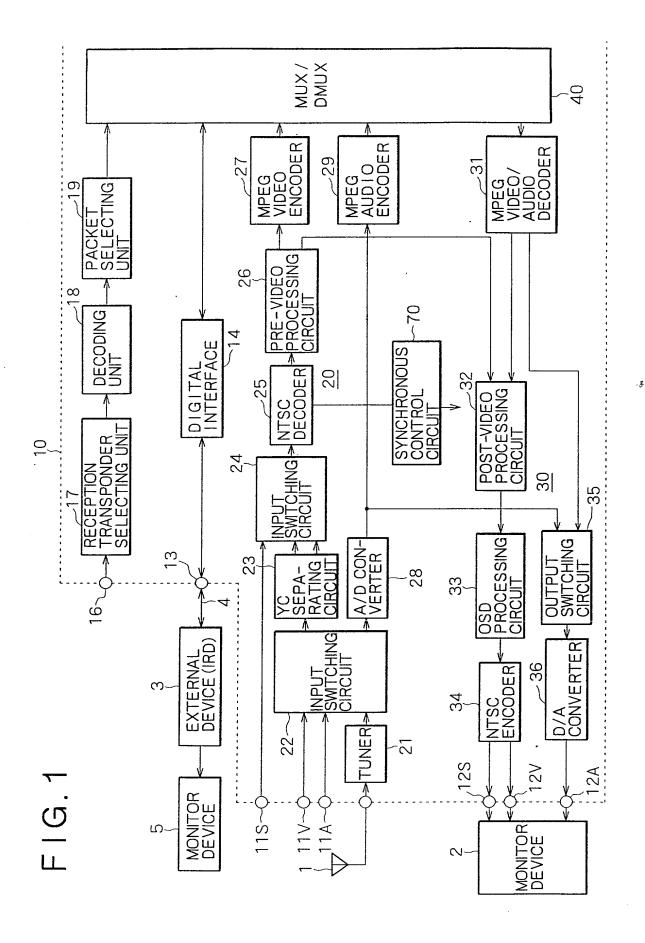
€ ×

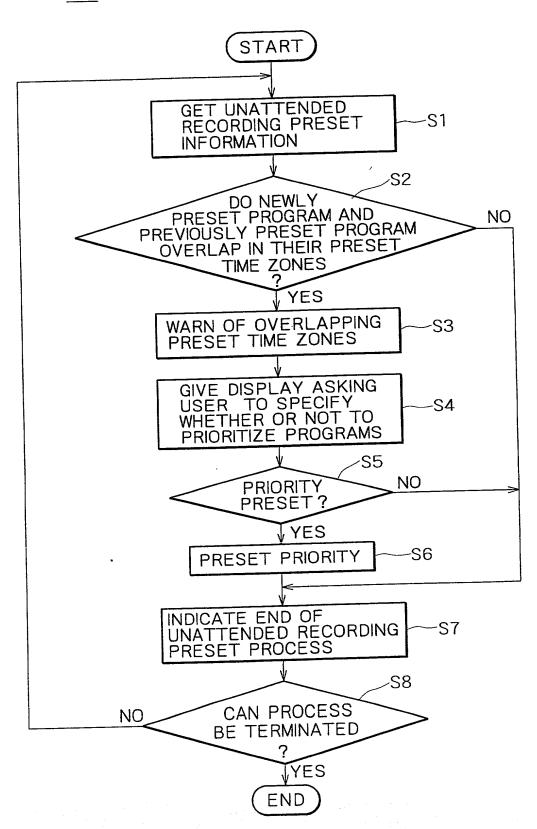


-134-132 133 -131 0 0 O CPU 8 .720 -62 -63 -61 DRIVER 90 84 9 RAM 110 I/O INTERFACE OPERATION INPUT UNIT 82 88 150 83 BUFFER CONTROLLER 140 50 ROM F1G.2 DISPLAY CONTROL UNIT DISPLAY UNIT MUX/ DMUX 40 MPEG VIDEO/AUDIO MPEG VIDEO ENCODER MPEG AUDIO ENCODER PACKET SELECTING UNIT -29 727 731 DIGITAL INTERFACE σ 7

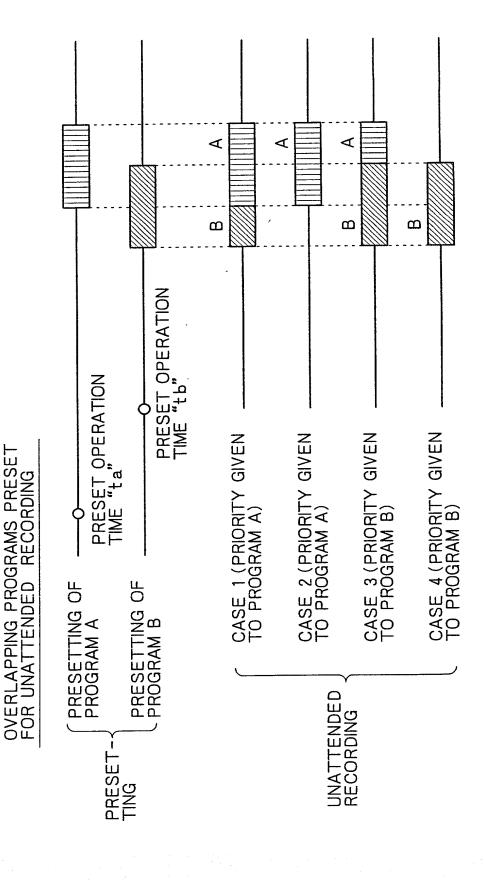
13

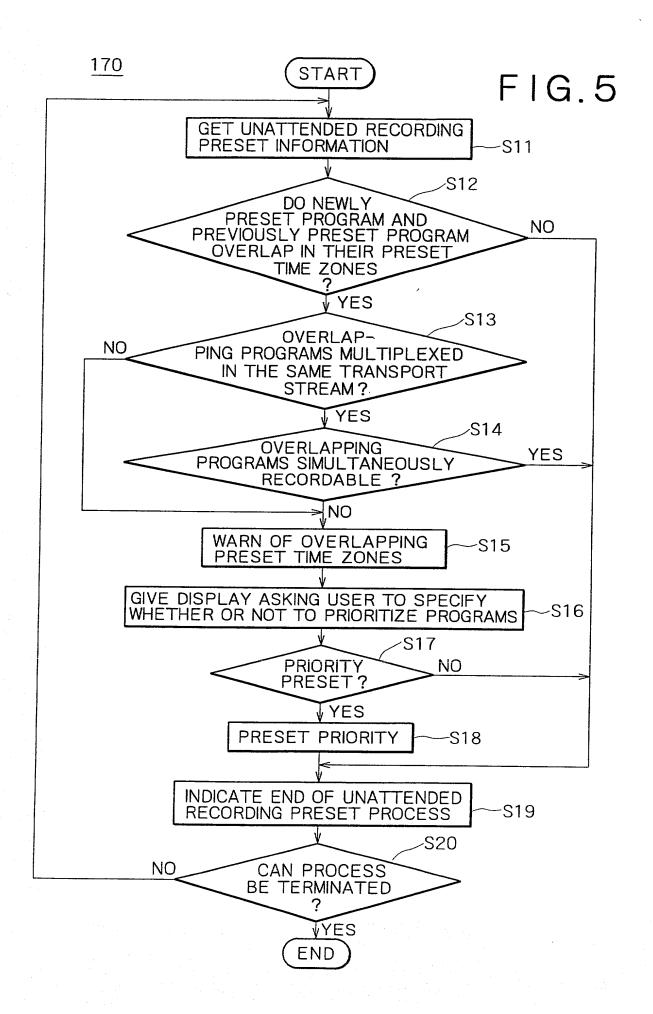
FIG.3





F1G.4





F1G.6

